

## BYV97 series

### FEATURES

- Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack.

### DESCRIPTION

Rugged glass SOD57 package, using a high temperature alloyed construction. This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.

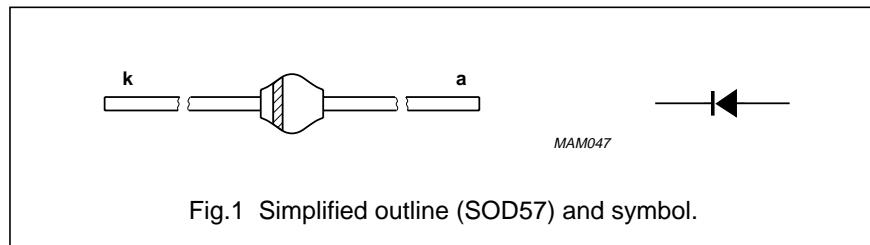


Fig.1 Simplified outline (SOD57) and symbol.

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{RRM}$	repetitive peak reverse voltage BYV97F BYV97G		—	1200 1400	V V
$V_R$	continuous reverse voltage BYV97F BYV97G		—	1200 1400	V V
$I_{F(AV)}$	average forward current	$T_{tp} = 60^\circ\text{C}$ ; lead length = 10 mm see Fig.2; averaged over any 20 ms period; see also Fig.6	—	1.6	A
$I_{F(AV)}$	average forward current	$T_{amb} = 50^\circ\text{C}$ ; PCB mounting (see Fig. 12); see Fig.3; averaged over any 20 ms period; see also Fig.6	—	0.9	A
$I_{FRM}$	repetitive peak forward current	$T_{tp} = 65^\circ\text{C}$ ; see Fig.4	—	15	A
		$T_{amb} = 65^\circ\text{C}$ ; see Fig.5	—	8	A
$I_{FSM}$	non-repetitive peak forward current	$t = 10 \text{ ms half sine wave}$ ; $T_j = T_{j \max}$ prior to surge; $V_R = V_{RRM\max}$	—	20	A
$E_{RSM}$	non-repetitive peak reverse avalanche energy	$L = 120 \text{ mH}$ ; $T_j = T_{j \max}$ prior to surge; inductive load switched off	—	10	mJ
$T_{stg}$	storage temperature		-65	+175	°C
$T_j$	junction temperature	see Fig.7	-65	+175	°C

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### ELECTRICAL CHARACTERISTICS

$T_j = 25^\circ\text{C}$  unless otherwise specified.

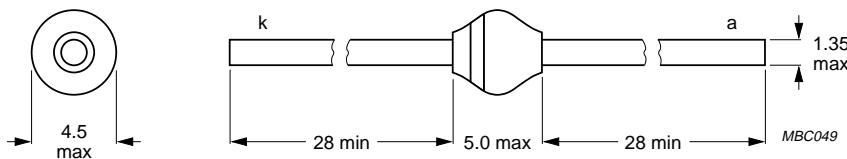
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_F$	forward voltage	$I_F = 3 \text{ A}; T_j = T_{j\max};$ see Fig.8			1.35	V
		$I_F = 3 \text{ A};$ see Fig.8	—	—	1.65	V
$V_{(BR)R}$	reverse avalanche breakdown voltage BYV97F BYV97G	$I_R = 0.1 \text{ mA}$	1300 1500	— —	— —	V V
$I_R$	reverse current	$V_R = V_{RRM\max};$ see Fig.9	—	—	1	$\mu\text{A}$
		$V_R = V_{RRM\max}; T_j = 165^\circ\text{C};$ see Fig.9	—	—	150	$\mu\text{A}$
$t_{rr}$	reverse recovery time	when switched from $I_F = 0.5 \text{ A}$ to $I_R = 1 \text{ A};$ measured at $I_R = 0.25 \text{ A};$ see Fig.14	—	—	500	ns
$C_d$	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V};$ see Fig.11	—	35	—	pF
$\left  \frac{dI_R}{dt} \right $	maximum slope of reverse recovery current	when switched from $I_F = 1 \text{ A}$ to $V_R \geq 30 \text{ V}$ and $dI_F/dt = -1 \text{ A}/\mu\text{s};$ see Figs 10 and 13	—	—	5	$\text{A}/\mu\text{s}$

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th j\text{-tp}}$	thermal resistance from junction to tie-point	lead length = 10 mm	46	K/W
$R_{th j\text{-a}}$	thermal resistance from junction to ambient	note 1	100	K/W

#### Note

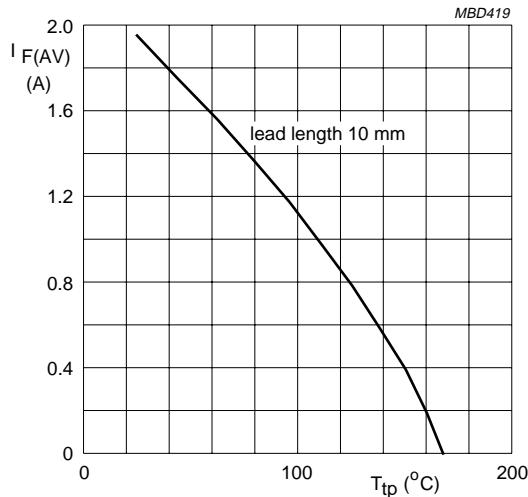
1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer  $\geq 40 \mu\text{m},$  see Fig. 12. For more information please refer to the "General Part of associated Handbook".



Dimensions in mm.

The marking band indicates the cathode.

Fig.12 SOD64.

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**GRAPHICAL DATA**


$$a = 1.57; V_R = V_{RRMmax}; \delta = 0.5.$$

Fig.2 Maximum permissible average forward current as a function of tie-point temperature (including losses due to reverse leakage).

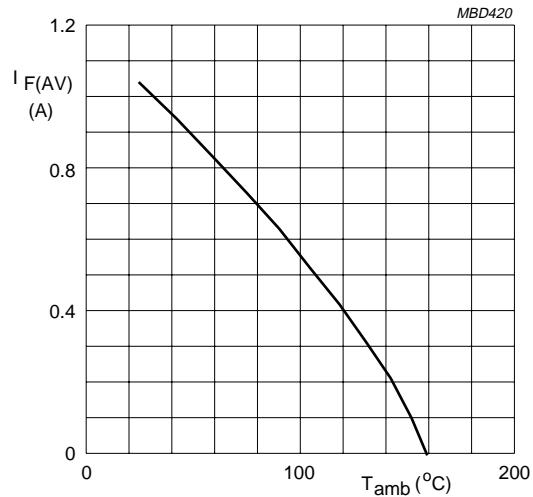
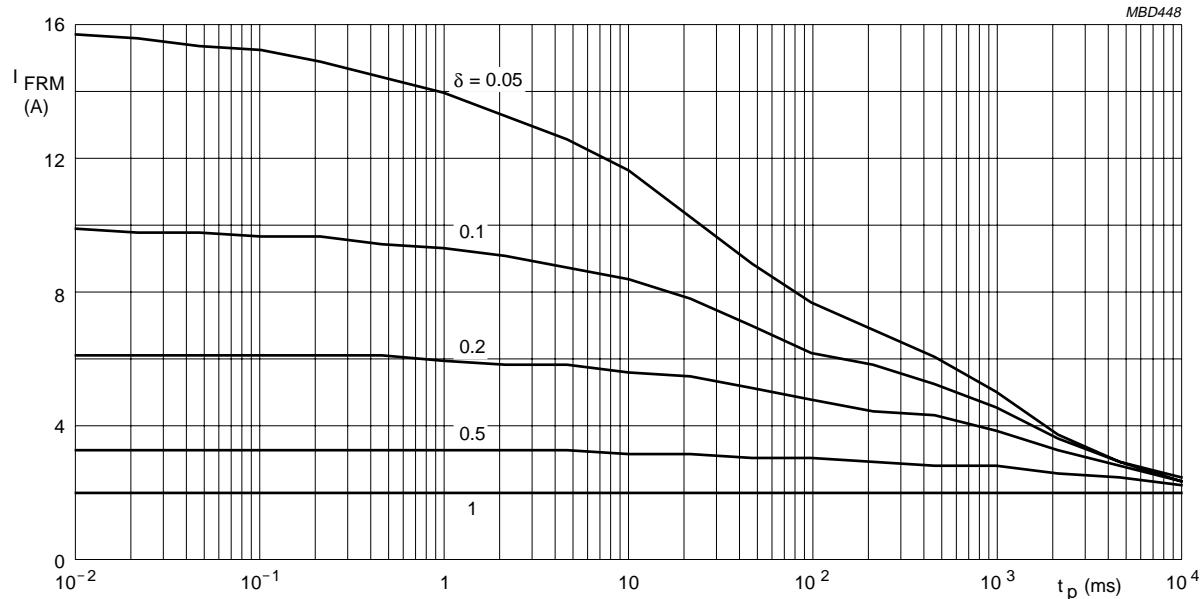


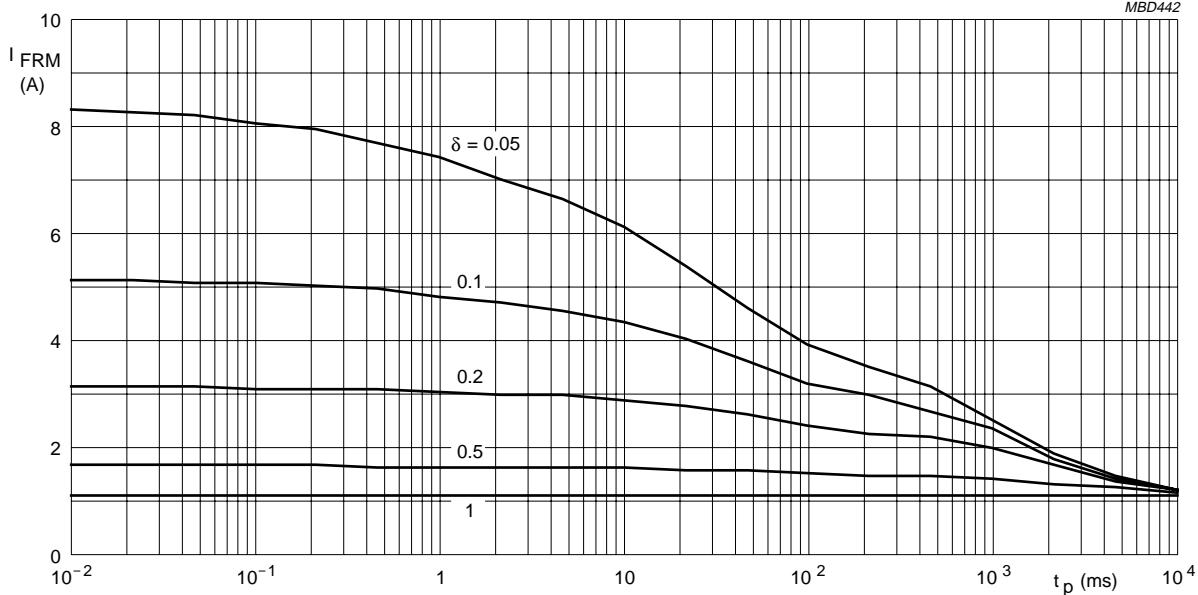
Fig.3 Maximum permissible average forward current as a function of ambient temperature (including losses due to reverse leakage).



$$T_{tp} = 65^{\circ}\text{C}; R_{th,j-tp} = 46 \text{ K/W.}$$

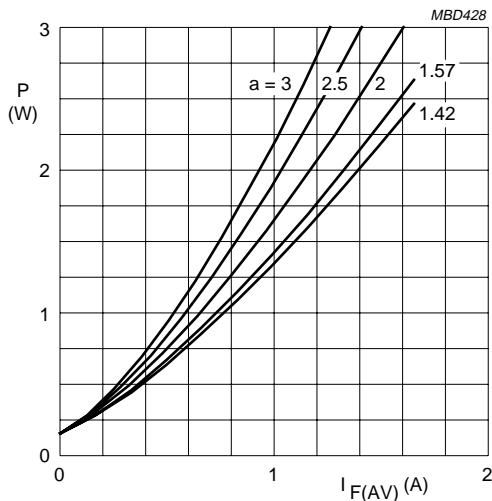
$V_{RRMmax}$  during  $1 - \delta$ ; curves include derating for  $T_{j\max}$  at  $V_{RRM} = 1400 \text{ V}$ .

Fig.4 Maximum repetitive peak forward current as a function of pulse time (square pulse) and duty factor.

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$T_{amb} = 65^\circ\text{C}$ ;  $R_{th,j-a} = 100 \text{ K/W}$ .  
 $V_{RRMmax}$  during  $1 - \delta$ ; curves include derating for  $T_{j,max}$  at  $V_{RRM} = 1400 \text{ V}$ .

Fig.5 Maximum repetitive peak forward current as a function of pulse time (square pulse) and duty factor.



$a = I_{F(RMS)} / I_{F(AV)}$ ;  $V_R = V_{RRMmax}$ ;  $\delta = 0.5$ .

Fig.6 Maximum steady state power dissipation (forward plus leakage current losses, excluding switching losses) as a function of average forward current.

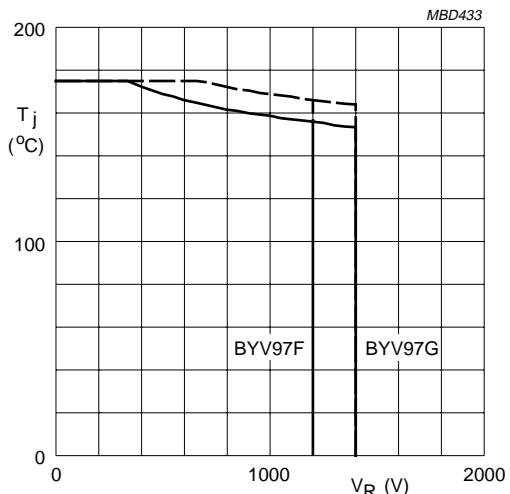
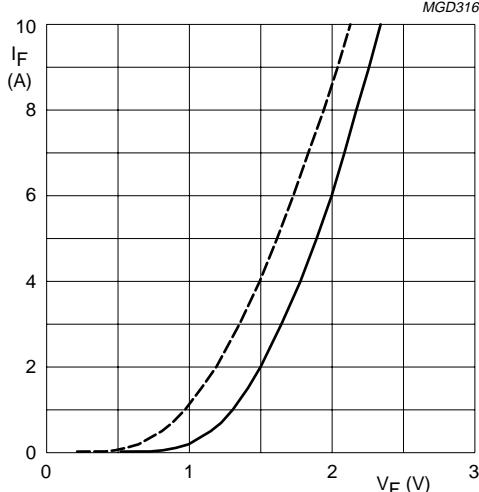
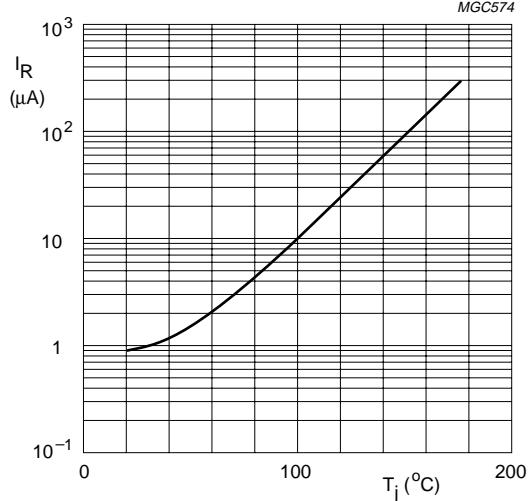


Fig.7 Maximum permissible junction temperature as a function of reverse voltage.

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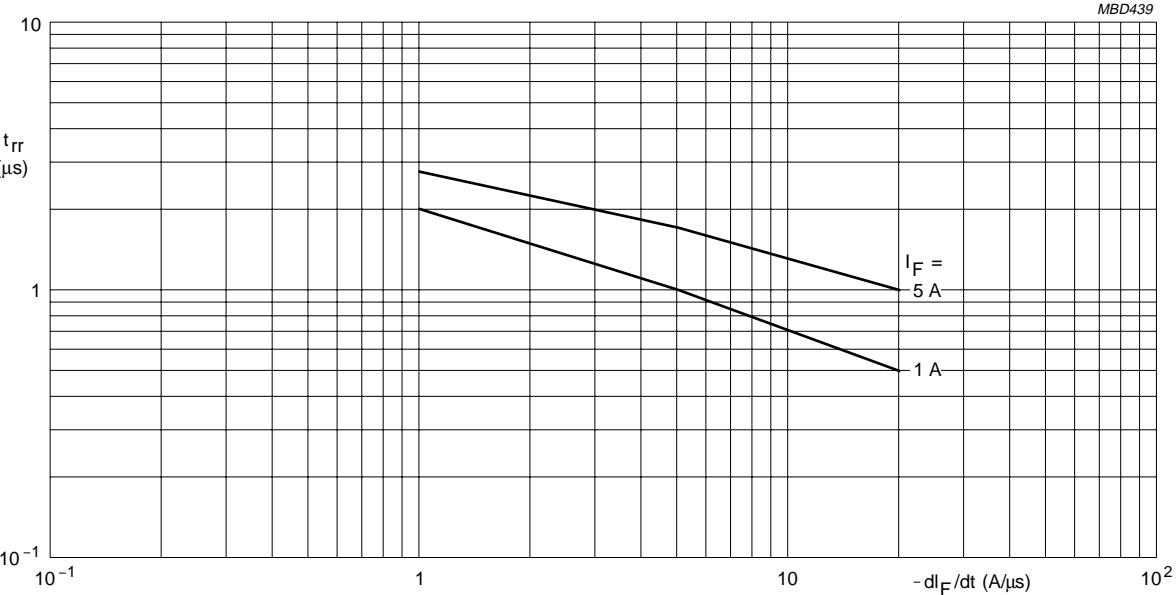
Dotted line:  $T_j = 175^\circ\text{C}$ .  
 Solid line:  $T_j = 25^\circ\text{C}$ .

Fig.8 Forward current as a function of forward voltage; maximum values.



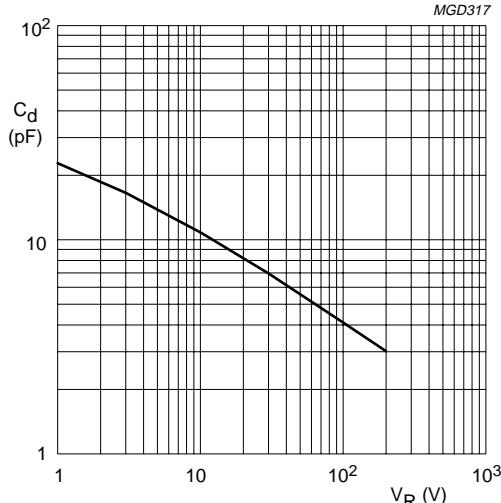
$V_R = V_{RRMmax}$ .

Fig.9 Reverse current as a function of junction temperature; maximum values.



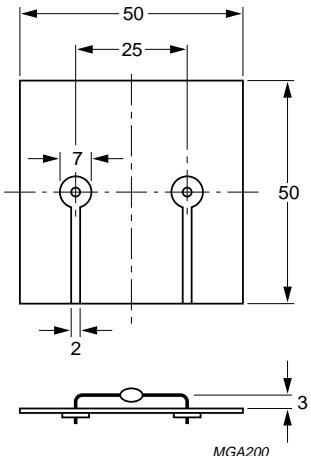
$T_j = 25^\circ\text{C}$ .  
 For definitions see Fig. 13.

Fig.10 Maximum reverse recovery time as a function of the rate of fall of forward current.

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$f = 1 \text{ MHz}; T_j = 25^\circ\text{C}$ .

Fig.11 Diode capacitance as a function of reverse voltage; typical values.



Dimensions in mm.

Fig.12 Device mounted on a printed-circuit board.

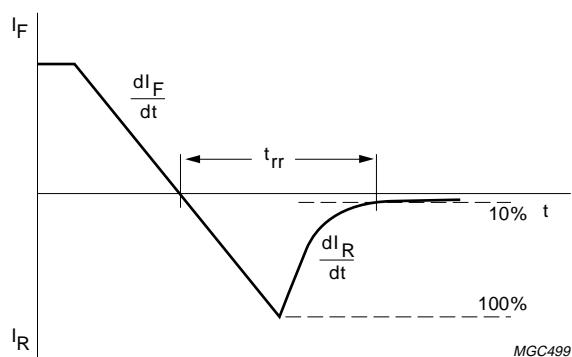
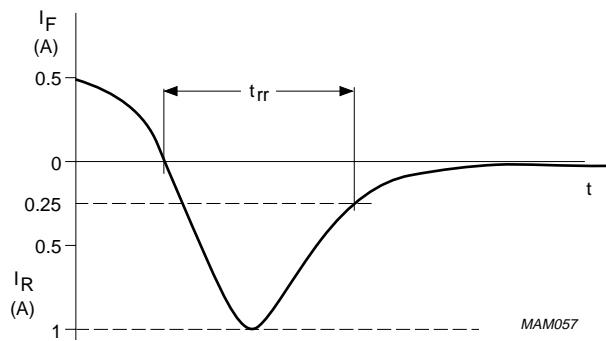
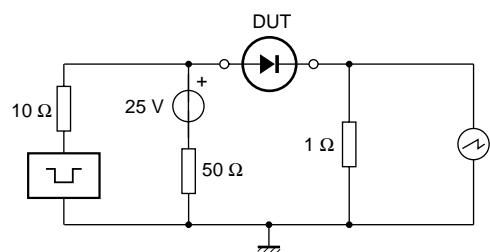


Fig.13 Reverse recovery definitions.

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Input impedance oscilloscope:  $1 \text{ M}\Omega$ ,  $22 \text{ pF}$ ;  $t_r < 7 \text{ ns}$ .

Source impedance:  $50 \Omega$ ;  $t_r < 15 \text{ ns}$ .

Fig.14 Test circuit and reverse recovery time waveform and definition.